



16310 U.S. PTO
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PATENT APPLICATION
Attorney's Do. No. 5038-311
Intel Ref No.: P17162

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

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LAUREN BALLARD-GEMMELL
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Lauren Ballard-Gemmell
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Mail Stop Patent Application
Commissioner for Patents
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Enclosed for filing is a patent application under 37 CFR 1.53(b) of:

Inventor: Shiva Aditham

For: **LOW COST COMPLIANCE TEST SYSTEM AND METHOD**

Applicant requests FIG. 2 to be published with the application.

Enclosures:

- Specification (pages 1-5); claims (pages 6-8); abstract (page 9)
- FOUR sheet(s) of FORMAL drawings
- Declaration or Combined Declaration and Power of Attorney
- Newly executed (original or copy)
- Assignment with cover sheet

Assignee Name and Address: Intel Corporation
2200 Mission College Blvd.
P.O. Box 58119
Santa Clara, California 95052-8119

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<u>CLAIMS AS FILED</u>				
For	Number Filed	Number Extra	Rate	Basic Fee \$770
Total Claims	24	4	x \$ 18 =	\$72
Independent Claims	4	1	x \$ 86 =	\$86
Multiple Dependent Claim Fee			x \$290 =	
TOTAL FILING FEE				\$928.00

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Respectfully submitted,

MARGER JOHNSON & McCOLLOM, P.C.



Graciela G. Cowger

Reg. No. 42,444

MARGER JOHNSON & McCOLLOM, P.C.
 1030 SW Morrison Street
 Portland, OR 97205
 503-222-3613